IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	09/823,386	Confirmation No. 7368
Applicant	:	Monte J. Rhodes	
Filed	:	March 30, 2001	
TC/A.U.	:	2645	
Examiner	:	Elahee, Md S.	
Docket No.	:	42390P11045	
Customer No.	:	008791	

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Declaration of Greg D. Caldwell
Pursuant to 37 C.F.R. §1.132

Sir:

I, Greg D. Caldwell, hereby declare that:

1. I am a citizen of Canada, and currently reside in Newberg, Oregon.

2. I am currently a partner of the law firm Blakely, Sokoloff, Taylor & Zafman.

3. I have worked as an attorney at Blakely, Sokoloff, Taylor & Zafman continuously since July 1995.

4. Drafting of the application for the subject invention was assigned to me on February 2, 2001.

5. I have reviewed the enclosed copy of Monte Rhodes' 37 C.F.R. § 1.131 declaration and the associated invention disclosure (disclosure number 17414; docket number P11045), filed with the response of May 9, 2005, and the docket note assigning the drafting work for the disclosure to me dated February 2, 2001 (please see attached Intel Disclosure

and Foreign Filing Information Form, "Notes"). I certify the copy of the docket note is a true copy of the original and that disclosure 17414 was the basis of the application I drafted and filed on March 30, 2001.

6. I, and Joe Pugh, another attorney then with Blakely, Sokoloff, Taylor and Zafman, pursued drafting of the original application with reasonable diligence in view of our previously existing case load from at least February 2, 2001 until the filing date of March 30, 2001.

7. I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-identified patent application or any patent issued thereon.

Executed on: Oct. 21, 2005

At: Beaverton, Oregon

By: 

Greg D. Caldwell

Matter #: **P11045** Intel Grp Atty: **JFK/INTEL** Work Atty: GDC/BSTZ

Matter Status: **IN PROCESS**

TYPE OF INTEL PATENT APPLICATION FILE

*Patent: ☒ **Utility** ☐ **Design** ☐ **Reissue** ☐ **Reexam** ☐ **CPA (C)** ☐ **CIP (X)** ☐ **Divisional (D)**

Title of File: **SERVER APPLIANCE WIRELESS INTERFACE**

INTEL DISCLOSURE AND FOREIGN FILING INFORMATION

*Disclosure number(s): **17414**

*Product/Process: [redacted]

Intel Committee: **COMMUNICATION SYSTEMS & SOFT** Intel Group: **CPG** Intel Division: **IMAD**

F-Filing: **NO** Ctry:

Notes: **P11045 (17414) OPENED & ASSIGNED TO GREG CALDWELL PER EMAIL FROM JB 2/2/01 AL.**

*INTEL ABSTRACT CODES (Check One or More)

__PROCESS (C1)
- __N or P MOS (C1A)
- __Equipment (C1B)
- __CMOS (C1C)
- __Contacts (C1D)
- __Flash (C1E)
- __GaAs and SOS (C1F)
- __Circuit element (C1G)
- __Isolation/Insulation (C1H)
- __BiCMOS (C1I)
- __Analysis/Testing (C1J)
- __Etching/Planarization (C1K)
- __Metal (C1L)
- __Poly silicon (C1M)
- __Passivation (C1N)
- __Masking/Resist (C1O)
- __Deposition (C1P)
- __Implantation (C1Q)

__DRAMs (C2)
- __Sense amp (C2A)

__SRAMs (C3)
- __Sense amp (C3A)

__EPROMS (C4)
- __P-channel (C4A)
- __N-channel (C4B)
- __Flash (C4C)
- __EE (C4D)
- __Sense amp (C4E)
- __Solid-State disk (C4F)
- __Flash Card (PCMCIA) (C4G)
- __Multibit Cell (C4H)
- __Redundancy (C4I)
- __Blocking (C4J)
- __Write Automation (C4K)
- __Minicard (C4L)
- __ Camera (C4M)
- __ FMM (C4N)
- __ Firmware Hub (FWH) (C4O)
- __ Security (C4P)
- __ Small Block (C4Q)
- __ FDI (C4R)
- __ Interface (C4S)
- __ Connector (C4T)
- __ Cell Phone (C4U)
- __ Charge Pump (C4V)
- __ Audio (C4W)

__ Microprocessor (C5)
- __ Embedded (C5A)
- __Buses Input/Output Devices (C5B)
- __Protocol/CPU Interfacing (C5C)
- __Adder/Multiplier Units (C5D)
- __Numeric (C5E)
- __Video/Graphics (C5F)
- __Cache/memory Hierachy/ Memory/Virtual Memory (C5G)
- __Memory Management/ Protection/Addressing (C5H)
- __Instruction/Inst. Decoding/ Microcoding/Sequencing/ Microprogrammed Control (C5I)
- __Pipeline/Parallelism (C5J)
- __Clocking/Clock Generation/ Clock Multiplication (C5K)
- __Addressing/Addressing Modes (C5L)
- __Vector Processing (C5M)
- __Registers/Files/Stacks (C5N)
- __Multiprocessing/Dual (C5O)
- __Initialization/Testing/ Debugging (C5P)
- __Program/Program Control/ Interrupt/Status/Faults Exceptions (C5Q)
- __RISC (C5R)
- __Redundancy (C5S)

__SYSTEMS (C6)
- __Bus (C6A)
- __Supercomputers (parallel multiprocessors) (C6B)
- __Compilers (C6C)
- __Test Equipment (ICE) (C6D)
- __BIOS (C6E)
- __PCMCIA (thin removable functionality cards, i.e., memory, modem, network, etc.) (C6F)
- __Magnetics (bubble memories) (C7)
- __Buffers (C8)
- __Packaging/Mounting/ Connector (C9)
- __Logic (C10)
- __Neural (C11)
- __Miscellaneous (C12)
- __General Memories (C13)
  - __Redundancy (C13A)
  - __Rambus-compatible (C13B)
- __General Circuit (C14)
- __Peripherals (C15)
- __ROM (C16)
- __Timing Clocks (C17)
- __Power/Regulation (C18)
- __Networks (C19)
- __PLD (C20)
- __Compression/Decompression (C21)
- __Video/Graphics/Audio (C22)
  - __ Algorithm (C22A)
  - __ System (C22B)
  - __ Sensor (C22C)
  - __ Optics (C22D)
  - __ 3D (C22E)
  - __ Display (C22F)
  - __ Graphics Device (C22G)
- __Test Equipment (C23)
- __Video Teleconferencing (C24)
- __Communication (C25)
- __ Software (C26)
  - __ Graphics (C26A)
  - __ Audio (C26B)
  - __ Compiler (C26C)
  - __ Operating System (C26D)
  - __ Drivers (C26E)
  - __ Other (C26F)
- __ IAL (C27)
  - __ Internet/WWW Applications (C27A)
  - __ Java Applics. (C27B)
  - __ User Interfaces Consumer (C27C)
  - __ Appliances Portable (C27D)
  - __ Computing (C27E)
- __ Compilers (C28)
  - __ Java Compilers (C28A)
  - __ Java Just-in-Time (C28B)
  - __ IA64 Compilers (C28C)
  - __ Optimization (C28D)
- __ Circuits (C29)
  - __ New Logic Family (C29A)
  - __ Data Path (C29B)
- __ Chipsets (C30)
  - __ Memory Control (C30A)
  - __ Bridging (C30B)
  - __ Firmware Hub (C30C)
- __ Design Tools (C31)
  - __ Circuits (C31A)
  - __ Layout (C31B)
  - __ Logic (C31C)
  - __ Validation/Test (C31D)
  - __ Low Power (C31E)

continued next page...

*Mandatory for original patent application. File will not be opened unless mandatory information is provided.

*INTEL ABSTRACT CODES (CONTINUED)

__CIRCUIT (C32)
__A/D (C32A)
__D/A (C32B)
__Amplifier (C32C)
__ OP (Opperational) (C32C2)
__ RF (Radio Frequency) (C32c3)
__Isolator (C32D)
__Receiver (C32E)
__Jitter Attenuator (C32E2)
__FM Demodulator (C32E3)
__Antenna Interface (C32E4)
__Line Driver (C32F)
__PLL (C32G)
__Frequency Multiplier (C32G2)
__Time Recovery (C32H)
__Filter (C32I)
__Adaptive (C32I2)
__Switched Capacitor (C32I3)
__Equalizer (C32I4)
__Echo Cancelier (C32I5)
__Detector (C32J)
__Signal Generator (C32K)
__Oscillator (C32L)
__TEST (C32M)
__BIST (BUILTIN-S-TEST) (C32M2)
__CODING/MODULATION. (C33)
__Viterbi (C33A)
__Block (C33B)
__Trellis (C33C)
__FM (C33D)
__QAM (C33E)
__HUB/REPEATER (C34)
__Ethernet (C34A)
__MAC (C34A2)
__PHY (C3A3)
__Ring (C34B)
__MODEM (C35)
__ Cable (C35A)
__ DSL (C35B)
__ PSTN (C35C)
__ Voice and Data (C35C2)
__ Wireless (C35D)
X NETWORK MANAGEMENT (C36)
__ Agent (C36A)
__ Network Discovery (C36B)
__ Network Topology (C36C)
__ Fault Tolerance (C36C2)
__ Policy Based Management (C36D)
__ PROXY *C36E)
__ Software Distribution (C36F)
__Virus Protection (C36G)
__ NETWORK OS (C37)
__ NIC (C38)
__Architecture (C38A)
__Bus Master (C38A2)
__ATM (C38B)
__Device Driver (C38C)
__Ethernet (C38D)
__MAC (C38D2)
__PHY (C38D3)
__Media Attachment (C38D4)
__Media Independent Interface (C38D5)
__ NETWORK PROCESSOR (C39)
__Multi-threaded (C39A)
__Architecture (C39B)
__Instruction set (C39B2)
__Compiler (C39C)
__Bus (C39D)
__Memory (C39E)
__Micro-architecture (C39F)
__Memory Controller (C39G)
__Switch (C39H)
__Debugging (C39I)
X NETWORK COMM. _PROTOCOLS (C40)
X Internet (C40A)
__Audio or Video (C40B)
__Web Caching (C40C)
__Bus. Method (C40D)
__Wireless (C40E)
__Home Networking (C40F)
__Phone Line (C40F2)
__Power Line (C40F3)
__Wireless (C40F4)

__ SWITCH/ROUTER (C41)
__ATM (C41A)
__Ethernet (C41B)
__MAC (C41B2)
__PHY (C41B3)
__Load Balanceer (C41C)
__XML (C41D)
__Routing (C41E)
__ SECURITY (C42)
__Cryptography (C42A)
__Smartcard (C42B)
__VPN (C42C)
__Access Control (C42D)
__ TELEPHONY (C43)
__Call Controll _Features (C43A)
__Circuits (C43B)
__Fax (C43C)
__ISDN (C43D)
__Bridge (C43D2)
__PBX (C43E)
__Video Conferecing (C43F
__Voice/Speech Processing (C3G